

10/646, 3ES

6 a second input buffer;
7 a second signal line termination;
8 a second signal line coupled with said second input buffer and said second
9 signal line termination, said second signal line to receive a second
10 signal;
11 a first termination node coupled with said first signal line termination and
12 said second signal line termination, said first termination node having
13 a termination potential, said termination potential providing as a
14 reference potential to said first input buffer and said second input
15 buffer;
16 a third input buffer;
17 a third signal line coupled with the third input buffer, said termination
18 potential providing a reference potential to said third input buffer,
19 said third signal line to receive a third signal; and
20 a second termination node coupled to the third signal line.

1 35. An apparatus as in claim 34, further comprising:
2 a fourth input buffer; and
3 a fourth signal line coupled with the fourth input buffer and the second
4 termination node, said termination potential providing a reference
5 potential to said fourth input buffer to receive a fourth signal/ ,

1 36. An apparatus as in claim 34, further comprising:

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